

45565



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	:
	:
Sung-Jin Park et al.	: Group Art Unit:
	:
Serial No.: 10/634,746	: Examiner:
	:
Filed: August 6, 2003	:
	:
For: TURBO DECODING APPARATUS	:

INFORMATION DISCLOSURE STATEMENT

COMMISSIONER FOR PATENTS  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

In accordance with 37 C.F.R. §§ 1.56 and 1.97-1.98, Applicants hereby transmit copies of the documents listed in the attached Form PTO-1449 which may be deemed material to the examination of the above-identified application. A copy of a International Search Report citing these documents is also being transmitted for the Examiner's information.

Since the present Information Disclosure Statement is being filed prior to issuance of a first Office Action on the merits, no certification under 37 C.F.R. § 1.97(e) or fee under 37 C.F.R. § 1.17(b) is required.

The Examiner is requested to consider the attached documents in connection with the above-identified application and to return a copy of Form PTO-1449 to the Applicants with the Examiner's initials in the spaces provided.

Submission of this Information Disclosure Statement does not constitute an admission by the Applicants as to the materiality of the attached documents to the application, nor do the Applicants waive any right to challenge the validity of the documents as prior art should such action be deemed appropriate.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Peter L. Kendall", written over a horizontal line.

Peter L. Kendall  
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Dated: March 23, 2004

U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICE

(Use several sheets if necessary)

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10/634,746

Sung-Jin Park et al.

August 6, 2003

GROUP
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[illegible]

		Pub. No.	Pub. Date	Patentee	CLASS	SUB-CLASS	FILING DATE

[illegible]

		Mohammad M. Mansour et al., "Design Methodology for High-Speed Iterative Decoder Architectures", <u>Proceeding of IEEE Conference on Acoustics, Speech, and Signal Processing</u> , Vol. 3, pages III-3085 - III-3088 (May 2002).
		Alexander Worm et al., "VLSI Architectures for High-Speed MAP Decoders", <u>Proceeding of 14<sup>th</sup> International Conference on VLSI Design</u> , pages 446-453 (January 2001).

\*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw Line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.